

JG2D75P120FG

Product Preview

**1200V/75A PIM WITH
FIELD-STOP TRENCH IGBT, DIODE AND NTC**

Features

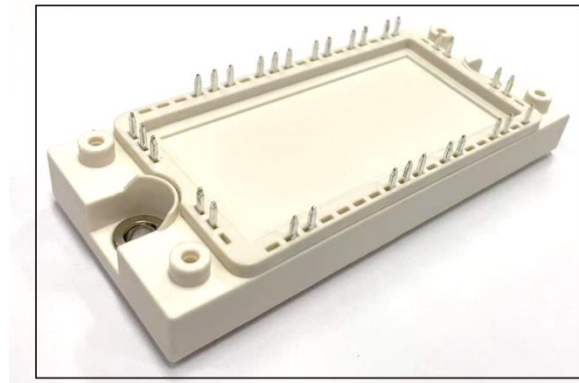
- Low $V_{CE(sat)}$
- Fast Switching
- High Ruggedness
- Short-Circuit Rated



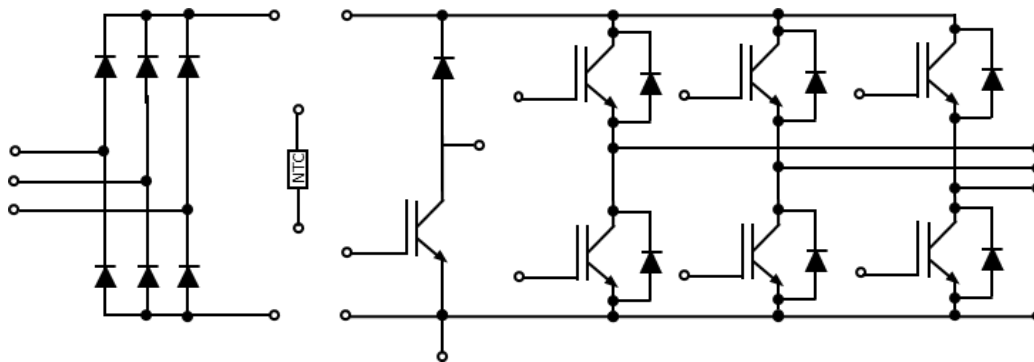
Product Summary	
V_{CES}	1200V
I_C	75A
$V_{CE(sat),typ}$	1.65V ($T_J = 25^\circ C$)

Applications

- General Purpose Inverters
- Frequency Converters
- Industrial Motor Drives
- Servos



Internal Connection



• **IGBT, Inverter**

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit
Collector-to-Emitter Voltage	V_{CES}	1200	V
Gate-to-Emitter Voltage	V_{GES}	± 20	
Continuous DC Collector Current ($T_c = 100^\circ C, T_J = 175^\circ C$)	I_{CDC}	75	A
Repetitive Peak Collector Current ($t_p=1ms$)	I_{CRM}	150	

Electrical Characteristics ^{(1), (2)}

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector-to-Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0V, I_C = 250\mu A$	1200	-	-	V
Collector-to-Emitter Leakage Current	I_{CES}	$V_{CE} = 1200V, V_{GE} = 0V$	-	-	5	mA
Gate-to-Emitter Leakage Current	I_{GES}	$V_{CE} = 0V, V_{GE} = \pm 20V$	-	-	400	nA
Gate Threshold Voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}, I_C = 1.5mA$	5.5	6.5	7.5	V
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = 15V, I_C = 75A$	-	1.65	2.0	
		$V_{GE} = 15V, I_C = 75A, T_J = 125^\circ C$	-	2.05	-	
		$V_{GE} = 15V, I_C = 75A, T_J = 150^\circ C$	-	2.2	-	
Total Gate Charge	Q_g	$V_{CC} = 600V, V_{GE} = 0/15V, I_C = 75A$	-	0.33	-	μC
Internal Gate Resistance	R_{Gint}	-	-	4.0	-	Ω
Input Capacitance	C_{iss}	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	-	7.33	-	nF
Output Capacitance	C_{oss}		-	0.29	-	
Reverse Transfer Capacitance	C_{rss}		-	0.1	-	
Turn-on Delay time	$t_{d(ON)}$	$V_{CC} = 600V, V_{GE} = \pm 15V, R_G = 2\Omega, I_C = 75A, L_{load} = 0.82mH, \text{Energy losses include "tail" and diode reverse recovery.}$	-	134	-	ns
Rise Time	t_r		-	65	-	
Turn-off Delay time	$t_{d(OFF)}$		-	220	-	
Fall Time	t_f		-	117	-	
Turn-On Switching Loss	E_{on}		-	5.94	-	
Turn-Off Switching Loss	E_{off}	-	3.39	-		
IGBT Total Switching Loss	E_{ts}	-	9.33	-		
Turn-on Delay time	$t_{d(ON)}$	$V_{CC} = 600V, V_{GE} = \pm 15V, R_G = 2\Omega, I_C = 75A, L_{load} = 0.82mH, \text{Energy losses include "tail" and diode reverse recovery. } T_J = 150^\circ C$	-	138	-	ns
Rise Time	t_r		-	68	-	
Turn-off Delay time	$t_{d(OFF)}$		-	224	-	
Fall Time	t_f		-	140	-	
Turn-On Switching Loss	E_{on}		-	9.43	-	mJ
Turn-Off Switching Loss	E_{off}		-	4.26	-	
IGBT Total Switching Loss	E_{ts}		-	13.69	-	
Short Circuit Collector Current	$I_{C(SC)}$	$V_{GE} = 15V, V_{CC} \leq 600V, t_{SC} \leq 10\mu s$	-	370	-	A

- **Diode, Inverter**

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit
Repetitive Peak Reverse Voltage	V_{RRM}	1200	V
Continuous DC Forward Current ($T_c = 100^\circ\text{C}$, $T_j = 175^\circ\text{C}$)	I_F	75	A
Repetitive Peak Forward Current ($t_p=1\text{ms}$)	I_{FRM}	150	

Electrical Characteristics ⁽¹⁾

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Diode Forward Voltage	V_F	$I_F = 75\text{A}$	-	2.0	2.5	V
		$I_F = 75\text{A}$ $T_j = 125^\circ\text{C}$	-	1.75	-	
		$I_F = 75\text{A}$ $T_j = 150^\circ\text{C}$	-	1.7	-	
Diode Reverse-Recovery Charge	Q_{rr}	$V_R = 600\text{V}$, $I_F = 75\text{A}$, $di_F/dt = -920\text{ A}/\mu\text{s}$	-	2.6	-	μC
Diode Peak Reverse-Recovery Current	I_{rrm}		-	29	-	A
Diode Reverse-Recovery Loss	E_{rr}		-	0.77	-	mJ

- **IGBT, Brake-Chopper**

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit
Collector-to-Emitter Voltage	V_{CES}	1200	V
Gate-to-Emitter Voltage	V_{GES}	± 20	
Continuous DC Collector Current ($T_c = 100^\circ\text{C}$, $T_j = 175^\circ\text{C}$)	I_{CDC}	50	A
Repetitive Peak Collector Current ($t_p=1\text{ms}$)	I_{CRM}	100	

Electrical Characteristics ^{(1), (2)}

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector-to-Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0\text{V}$, $I_C = 250\mu\text{A}$	1200	-	-	V
Collector-to-Emitter Leakage Current	I_{CES}	$V_{CE} = 1200\text{V}$, $V_{GE} = 0\text{V}$	-	-	5	mA
Gate-to-Emitter Leakage Current	I_{GES}	$V_{CE} = 0\text{V}$, $V_{GE} = \pm 20\text{V}$	-	-	100	nA
Gate Threshold Voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$, $I_C = 1.5\text{mA}$	5.5	6.5	7.5	V

Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = 15V, I_C = 50A$	-	1.7	2.0	
		$V_{GE} = 15V, I_C = 50A,$ $T_J = 125^\circ C$	-	2.05	-	
		$V_{GE} = 15V, I_C = 50A,$ $T_J = 150^\circ C$	-	2.15	-	
Total Gate Charge	Q_g	$V_{CC} = 600V,$ $V_{GE} = 15V,$ $I_C = 50A$	-	220	-	nC
Input Capacitance	C_{iss}	$V_{CE} = 25V,$ $V_{GE} = 0V,$ $f = 1MHz$	-	4150	-	pF
Output Capacitance	C_{oss}		-	230	-	
Reverse Transfer Capacitance	C_{rss}		-	54	-	
Turn-on Delay time	$t_{d(ON)}$	$V_{CC} = 600V,$ $V_{GE} = \pm 15V,$ $R_G = 10\Omega,$ $I_C = 50A,$ $L_{load} = 0.82mH,$ Energy losses include "tail" and diode reverse recovery.	-	41	-	ns
Rise Time	t_r		-	26	-	
Turn-off Delay time	$t_{d(OFF)}$		-	207	-	
Fall Time	t_f		-	127	-	
Turn-On Switching Loss	E_{on}	$V_{CC} = 600V,$ $V_{GE} = \pm 15V,$ $R_G = 10\Omega,$ $I_C = 50A,$ $L_{load} = 0.82mH,$ Energy losses include "tail" and diode reverse recovery. $T_J = 150^\circ C$	-	2.79	-	mJ
Turn-Off Switching Loss	E_{off}		-	2.4	-	
IGBT Total Switching Loss	E_{ts}		-	5.19	-	
Turn-on Delay time	$t_{d(ON)}$		-	36	-	
Rise Time	t_r	$V_{CC} = 600V,$ $V_{GE} = \pm 15V,$ $R_G = 10\Omega,$ $I_C = 50A,$ $L_{load} = 0.82mH,$ Energy losses include "tail" and diode reverse recovery.	-	28	-	ns
Turn-off Delay time	$t_{d(OFF)}$		-	204	-	
Fall Time	t_f		-	186	-	
Turn-On Switching Loss	E_{on}		-	4.22	-	
Turn-Off Switching Loss	E_{off}	$T_J = 150^\circ C$	-	3.19	-	mJ
IGBT Total Switching Loss	E_{ts}		-	7.41	-	

- **Diode, Brake-Chopper**

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit
Repetitive Peak Reverse Voltage	V_{RRM}	1200	V
Continuous DC Forward Current ($T_c = 100^\circ C, T_J = 175^\circ C$)	I_F	35	A
Repetitive Peak Forward Current ($t_p=1ms$)	I_{FRM}	70	

Electrical Characteristics ⁽¹⁾

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Diode Forward Voltage	V _F	I _F = 35A	-	2.0	2.4	V
		I _F = 35A T _J = 125°C	-	1.75	-	
		I _F = 35A T _J = 150°C	-	1.7	-	
Diode Reverse-Recovery Charge	Q _{rr}	V _R = 600V, I _F = 35A, dI _F /dt = -1590 A/μs	-	2.46	-	μC
Diode Peak Reverse-Recovery Current	I _{rrm}		-	34	-	A
Diode Reverse-Recovery Loss	E _{rr}		-	0.86	-	mJ

- **Diode, Rectifier**

Absolute Maximum Ratings ⁽¹⁾

Parameter	Symbol	Limit	Unit
Repetitive Peak Reverse Voltage	V _{RRM}	1600	V
Average Output Current 50/60Hz,sine wave (T _c = 100 °C)	I _{F(AV)}	75	A
Surge Forward Current (V _R =0, t _p =10ms, T _J = 25 °C)	I _{FSM}	600	

Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Diode Forward Voltage	V _F	I _F = 75A T _J = 150°C	-	0.9	-	V
Diode Reverse Current	I _R	V _R = 1600V T _J = 150°C	-	-	4.0	mA

- **NTC thermistors**

Characteristics

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Rated Resistance	R ₂₅	-	-	5.0	-	kΩ
Deviation of R100	ΔR/R	T _c = 100°C R ₁₀₀ = 493Ω	-5	-	5	%
Power Dissipation	P ₂₅	-	-	-	20.0	mW
B-value	B _{25/50}	$R_2 = R_{25} \exp [B_{25/50}(1/T_2 - 1/(298.15 K))]$	-	3375	-	K

- **Module**

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit
Maximum Junction Temperature	T_j	-40 to +175	°C
Operating Junction Temperature	$T_{vj\ op}$	-40 to +150	
Storage Temperature	T_{stg}	-40 to +150	
Isolation Voltage (f = 50 Hz, t = 1 min.)	V_{iso}	2.5	kV

Characteristics

Parameter	Symbol	Min	Typ	Max	Unit
Stray Inductance-module	L_{SCE}	-	35	-	nH
Module Lead Resistance, Terminal to Chip	$R_{CC'+EE'}$	-	4.0	-	mΩ
Module Lead Resistance, Terminal to Chip	$R_{AA'+CC'}$	-	3.0	-	
Junction-to-Case Thermal Resistance, per IGBT, Inverter	$R_{\theta JC}$	-	0.30	-	°C/W
Junction-to-Case Thermal Resistance, per Diode, Inverter		-	0.53	-	
Junction-to-Case Thermal Resistance, per IGBT, Brake-Chopper		-	0.46	-	
Junction-to-Case Thermal Resistance, per Diode, Brake-Chopper		-	0.97	-	
Junction-to-Case Thermal Resistance, per Diode, Rectifier		-	0.53	-	
Case-to-Heatsink Thermal Resistance, per IGBT, Inverter	$R_{\theta CH}$	-	0.15	-	°C/W
Case-to-Heatsink Thermal Resistance, per Diode, Inverter		-	0.27	-	
Case-to-Heatsink Thermal Resistance, per IGBT, Brake-Chopper		-	0.23	-	
Case-to-Heatsink Thermal Resistance, per Diode, Brake-Chopper		-	0.47	-	
Case-to-Heatsink Thermal Resistance, per Diode, Rectifier		-	0.29	-	
Case-to-Heatsink Thermal Resistance, per Module				0.01	
Module-to-Sink Torque	M_s	3.0	-	6.0	Nm
Weight per Module	G	-	180	-	g

(1) $T_j = 25^\circ\text{C}$ unless otherwise specified

(2) t_r : from 10% of I_c to 90% of I_c ; t_f : from 90% of I_c to 10% of I_c ;

E_{on} : from 10% of V_{GE} to 10% of V_{CE} ; E_{off} : from 90% of V_{GE} to 10% of I_c .

• Typical Electrical Characteristics

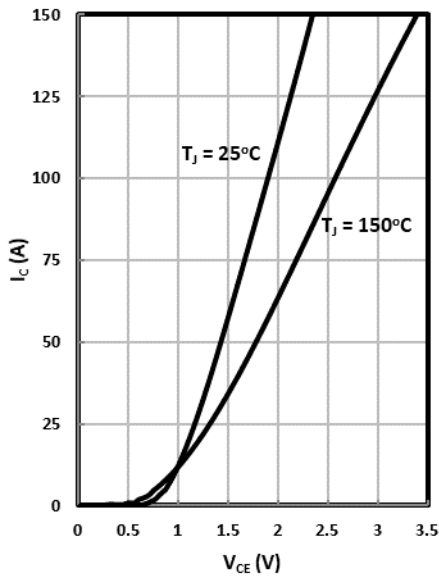


Fig. 1 IGBT (Inverter) Output Characteristics

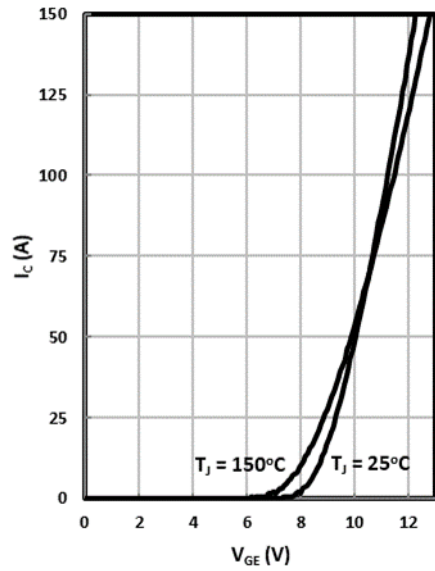


Fig. 2 IGBT (Inverter) Transfer Characteristics

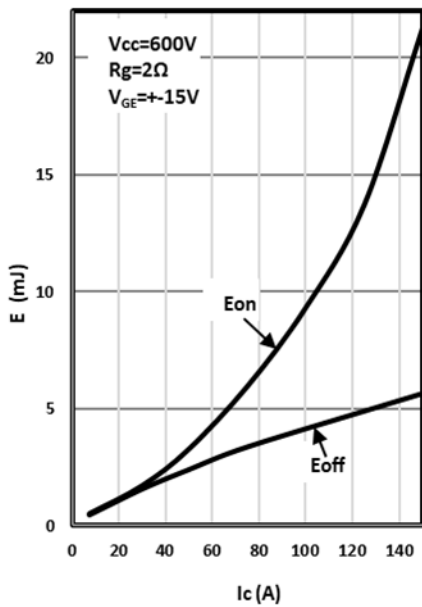


Fig. 3 IGBT (Inverter) Switching Loss vs. Ic

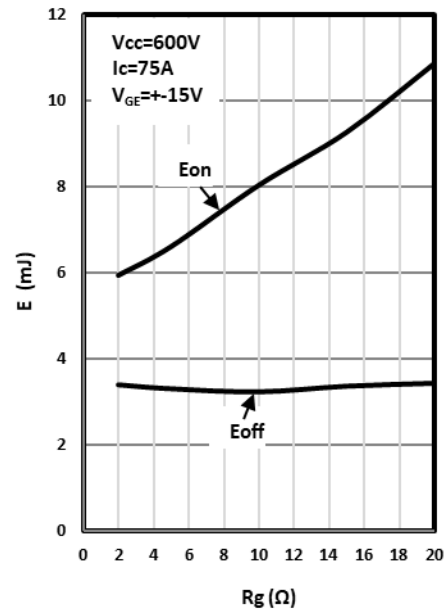


Fig. 4 IGBT (Inverter) Switching Loss vs. Rg



Fig. 5 RBSOA

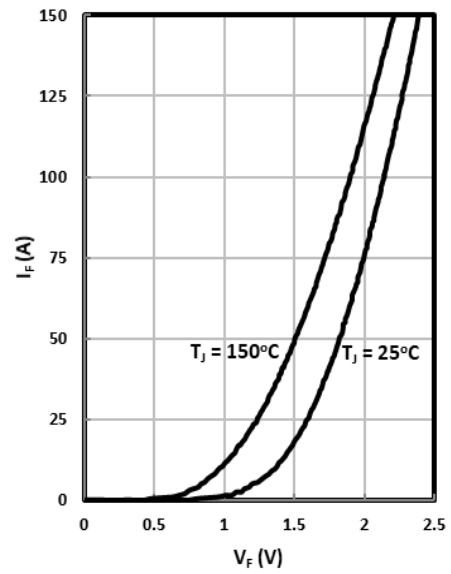


Fig. 6 Diode (Inverter) Forward Characteristics

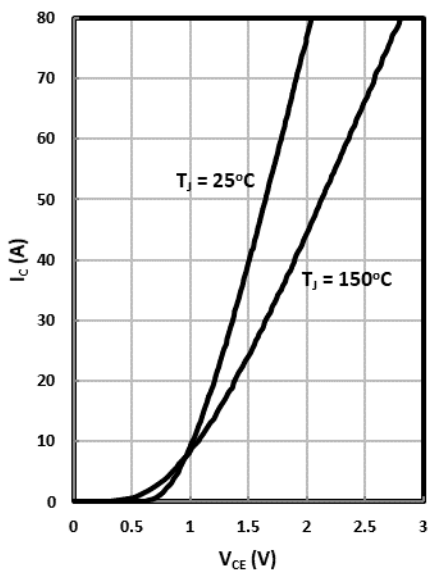


Fig. 7 IGBT (Brake-Chopper) Output Characteristics

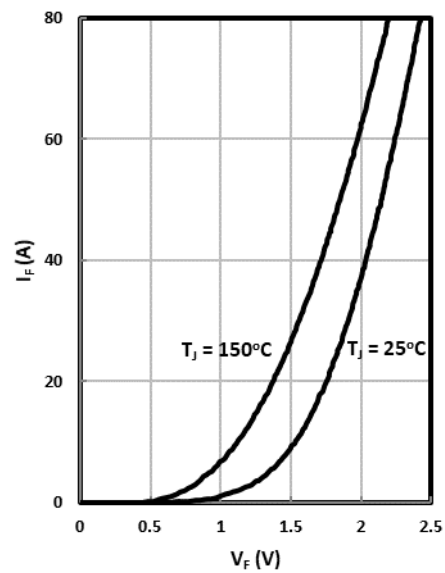


Fig. 8 Diode (Brake-Chopper) Output Characteristics

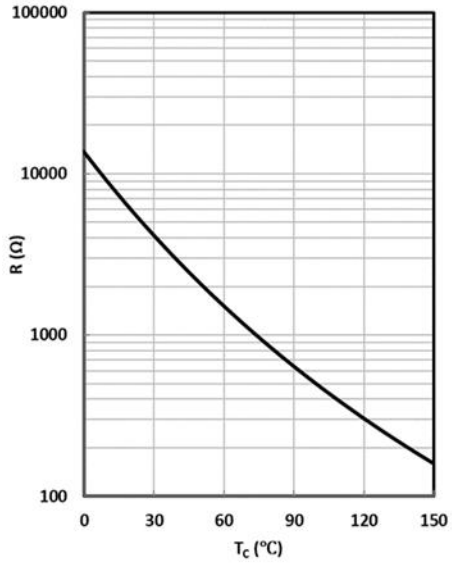
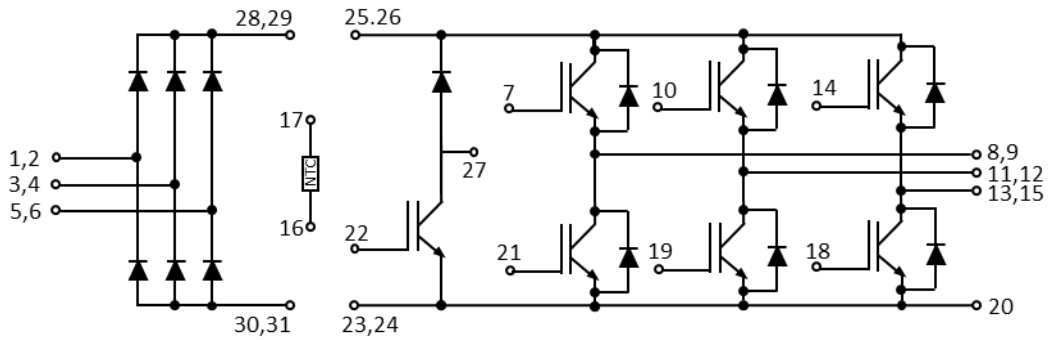
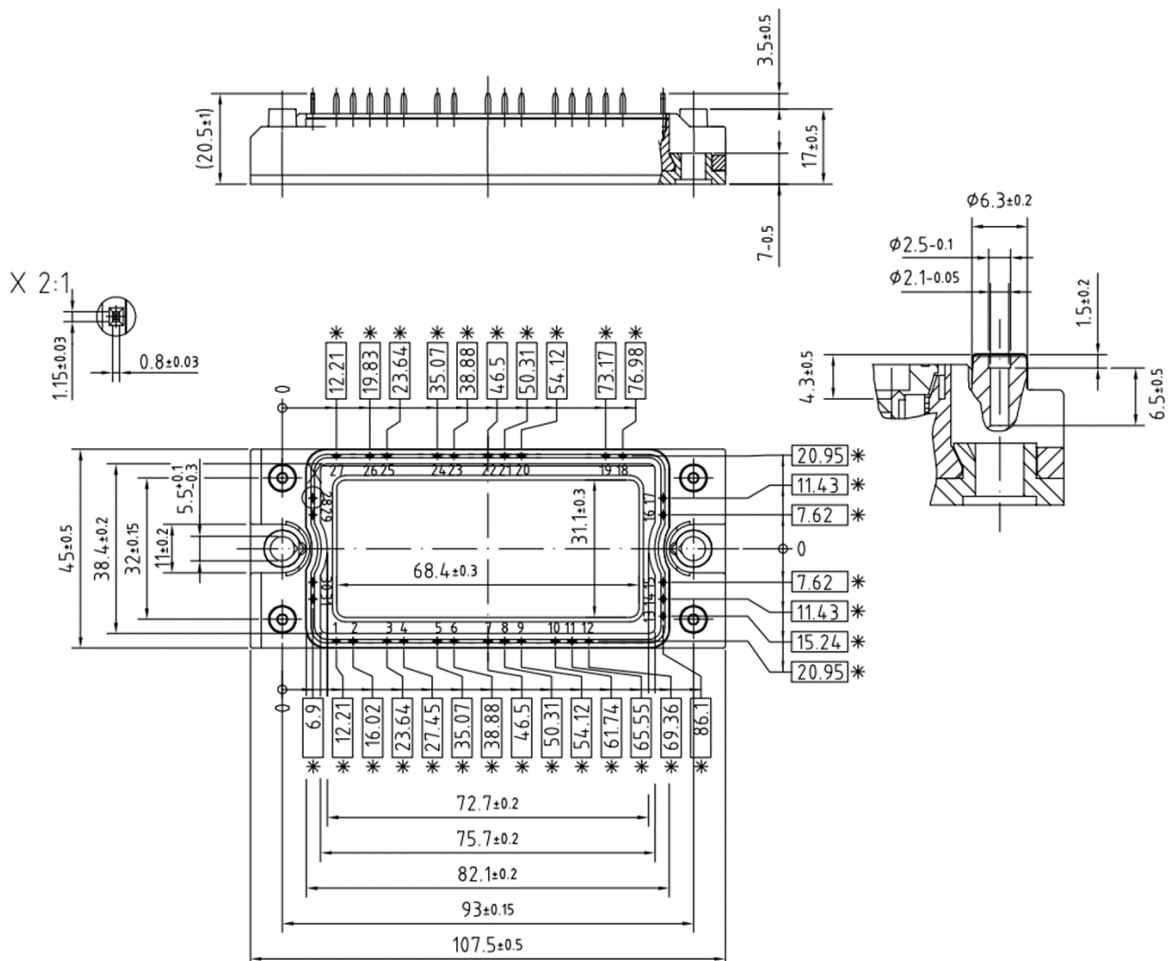


Fig. 9 NTC Temperature Characteristics

• Circuit diagram



• Package Dimensions



Notice

General – This document contains information on a product under development. Information in this document is believed to be accurate and reliable. However, JSAB Technologies does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes – JSAB Technologies reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use – JSAB Technologies' products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an JSAB Technologies product can reasonably be expected to result in personal injury, death or severe property or environmental damage. JSAB Technologies accepts no liability for inclusion and/or use of JSAB Technologies' products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications – Applications that are described herein for any of these products are for illustrative purposes only. JSAB Technologies makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values – Stress above one or more limiting values may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale – JSAB Technologies' products are sold subject to the general terms and conditions of commercial sale, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by JSAB Technologies. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license – Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control – This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Quick reference data – The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.